



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln Of: KIMURA
Serial No.: 10/091,776
Filed: March 5, 2002
For: CMOS REFERENCE VOLTAGE CIRCUIT
Group: 2816
Examiner: Englund, Terry Lee DOCKET: NEC NEG-244

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

In response to the Notice of Allowability mailed January 13, 2005, submitted herewith are ten (10) sheets of formal drawings with respect to the above-identified patent application.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP ISSUE FEE, Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450 on

April 6, 2005 at Tucson, Arizona.

By: SLB

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